

writing a data value to the SRAM cell on a first set of one or more bit lines, the first set of bit lines having a signal swing equal to the V_{cc} supply voltage;

reading data values from the SRAM cell on the first set of bit lines;

controlling the signal swing on the first set of bit lines to be equal to the V_{cc} supply voltage;

providing comparison data values to the CAM cell on a second set of bit lines; and

controlling the signal swing on the second set of bit lines to be less than the V_{cc} supply voltage.

REMARKS

Claims 1-32 have been allowed in the present application. Corrections to typographical errors have been made and no new matter has been added. If there are any questions, please telephone the undersigned at (408) 451-5903 to expedite prosecution of this case.

Respectfully submitted,



E. Eric Hoffman
Attorney for Applicant(s)
Reg. No. 38,186

Tel. No.: (408) 451-5903
Fax No.: (408) 451-5908

BEVER, HOFFMAN & HARMS, LLP
2099 Gateway Place, Suite 320
San Jose, CA 95110-1017

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